

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	113	712/30.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:40
L2	126	712/33.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:40
L3	163	712/37.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:40
L4	391	1 or 2 or 3	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:41
L5	1718116	configur\$5 or reconfigur\$5	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:46
L6	258	4 and 5	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:46
L7	23687	alu or (arithmetic adj logic)	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:48
L8	91	6 and 7	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:48
L9	30	(combinational or combinatorial or asynchronous\$2) and 8	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:49
L10	44537	combinational or combinatorial	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:50
L11	137	alu with 10	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:50
L12	3696	asynchronous\$2 near3 (memory or storage)	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:53
L13	295	10 and 12	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:53
L14	12	10 with 12	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:53
L15	31659	dma	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:53

L16	38	10 with 15	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:54
L17	703	10 with bus	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:54
L18	138	alu and 17	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:55
L19	143	10 with reconfigur\$6	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:55
L20	18	alu and 19	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:55
L21	4	reconfigurable near3 combinatorial	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:56
L22	4	reconfigurable near3 combinatorial	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:56
L23	40	reconfigurable with combinatorial	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:57
L24	227	interfac\$3 with combinational	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:57
L25	3	reconfigurable near3 combinational	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:58
L26	0	24 and asynynchronous\$3	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:58
L27	804	fpga with 10	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:58
L28	10	27 with reconfigur\$5	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:59
L29	10	27 and 28	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:59
L30	288	asynchronous near3 array	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 09:59
L31	2981	array adj processor	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 10:00

L32	13	30 and 31	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 10:00
L33	76	asynchronous near3 vector	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 10:01
L34	4	reconfigur\$6 and 21	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 10:01
L35	2	reconfigur\$6 and 33	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 10:01
L36	1	4 and 30	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 11:00
L37	370	712/32.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 11:00
L38	3	12 and 37	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 11:01
L39	234	5 and 37	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 11:01
L40	0	30 and 39	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 11:02
L41	22	10 and 39	US-PGPUB; USPAT; EPO	OR	OFF	2005/10/05 11:02